

Course Outline, Telecommunication Circuits, ELEC 4505, Fall 2007

- Lectures:** Mon, Wed 4:00-5:30, room 4499 ME
- Labs:** Wed, Thur, Fri 8:30, Odd Weeks, Room 4135ME
- Marks:** Labs 35% Assignments 15% Final 50% (must get at least 35/100 in final exam)
(As per standard practice in Engineering, students will not be allowed to see their final exam.)
* Academic accommodation for any reason must be sought as soon as possible, preferably early in the term. Verification will be required.

Course Objective To learn about the design of communications circuits. In other courses, the block diagrams have been seen, but here, emphasis will be on the actual circuitry which makes up these blocks. Examples of such blocks are tuned amplifiers, mixers, oscillators, phase shifters and detectors. Communications systems considered are wireless transceivers, AM, FM and TV. Use of the PLL will be discussed.

<u>Course Outline</u>	<u>Page</u>
1. Introduction to Telecommunications. Components of a radio systems; noise, distortion impedance matching.	1
2. Amplifier Design Tuned amplifiers, class C amplifiers, extension to frequency multipliers.	17
3. Mixers and Modulators	39
4. Phase-Locked Loop and Applications Introduction to PLLs and applications such as: synthesizers and FM demodulation.	47
5. Oscillators	72
6. Amplitude-Modulated Radio	83
7. Frequency Modulators and Demodulators	89
8. Television Systems Transmission and reception of video and audio; May also discuss high-definition TV, stereo sound.	102

Labs

Group size is 2 for all labs, one writeup per group, due one week after scheduled lab day, 4:30 PM.

- 1. Tuned Amplifiers (Simulation Lab)** (Sept. 26, 27, 28) **115**
Design and simulation of an 8 MHz tuned amplifier, built with a bipolar transistor and passive components. You will learn about use of transistor parameters, tuned circuits, noise figure and impedance matching.
- 2. Mixers and Modulators (Hardware Lab)** (October 10, 11, 12) **125**
Use of an analog multiplier on an IC to build frequency changers.
- 3. Phase-Locked Loops (Hardware Lab)** (October 24, 25, 26 and Novemer 7, 8, 9) **139**
Use of a commercially available package to build a tracking filter, a synthesizer and an FM demodulator. The IC contains a voltage-controlled oscillator a phase detector, and amplifiers. In this lab, the VCO and phase detector will be characterized, then a complete phased-lock loop will be built. The main external components will consist of a simple loop filter and a divider to realize the synthesizer.

References

- Smith, *Modern Communication Circuits 2nd Ed.*, McGraw-Hill 1998, TK6553.S5595
Krauss, Bostonian, Raab, *Solid State Radio Engineering*, Wiley 1980, TK6553.K73
Plett, Rogers, *Radio Frequency Integrated Circuit Design*, Artech House, 2003, TK7874
Sedra, Smith - (for intro to tuned amplifiers, oscillators)
Stremmler, *Introduction to Communication Systems* (or other intro texts)
Sington, *Linear Data Manual Volume 1: Communications* 1987 (or other date books)

Telecommunication Circuits ELEC 4505. 2004 Course Outline. Assignment 1 Comments, Solution (without numbers). The following calendar lists votes for exam review sessions (with second choice getting 0.7 vote). In the end, since Friday 5:30 and Monday 1:00 were both popular, reviews were held on both days. I didn't prepare problems to solve, instead, students put in requests and I went over these.Â Review Sessions: The first review session was on Friday Dec 17 5:30 in the room next to the IEEE. We went over two noise questions (2003, #1e, 2000 #2) one PLL question (2003 #5) and a mixer question (2003 #4). There were about 35 attendees (about half the class). The next review session was Monday 1:15 MC 5050 - more than half the class was there - an excellent turnout.